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November 13, 2000

VIA FIRST CLASS MAIL

Assistant Commissioner for Patents
Washington, D.C. 20231-9998

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TECHNOLOGY CENTER 2800

Re: Patent Application For:
Wafer-Scale Assembly of Chip-Size Packages.
Serial No.: 09/186,973
Attorney Docket No. TI-23158
Our File: 1000-2035

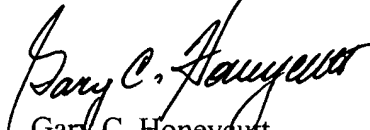
Dear Sir:

Enclosed please find the following items relating to the above-identified patent:

- (1) Amendment 37 CFR 1.115
- (2) Petition for Extension of Time; and
- (3) Postcards.

Please return the date-stamped postcard to our offices at the above address. In the meantime if you have any questions or comments concerning this matter, please call the undersigned at your earliest convenience. Otherwise, please accept the enclosed.

Very truly yours,


Gary C. Honeycutt
Reg. No. 20,250

GCH/ntn
Enclosures